

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

METHOD FOR MANUFACTURING SELF-COMPENSATING
RESISTORS WITHIN AN INTEGRATED CIRCUIT

Application Number :

Confirmation Number:

First Named Applicant: William Murphy

Attorney Docket Number: BUR920030090US1

Art Unit:

Examiner:

Search string: (5422298 or 5680092 or 6534374 or 5683566 or 6507993).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	5422298	1995-06-06	Jiminez			
	2	5680092	1997-10-21	Yamada et al			
	3	6534374	2003-03-18	Johnson et al			
	4	5683566	1997-11-04	Hetzler			
	5	6507993	2003-01-21	Dunn			

Signature

Examiner Name	Date